

JP11	Memory-Layout				
	0	1	2	3	
1	x		x		
2	x	x			
JP10	Wate-States				
	0	1	2	3	
1	x		x		MEM-Wait
2	x	x			
3	x		x		IO-Wait
4	x	x			
Memory-Layout	Sockel-Bereich				
	IC5-Sockel1	IC2-Sockel2	IC4-Sockel3	IC3-Sockel4	
0	0000..1FFF	2000..3FFF	4000..5FFF	6000..7FFF	
1	0000..7FFF		8000..FFFF		
2	0000..1FFF		2000..7FFF	8000..FFFF	
3	0000..3FFF	4000..7FFF	8000..FFFF		
		<i>E000.FFFF = Common Space bei CP/M</i>			
Mem-Layout 3 (IV):					
0000..3FFF	16K ROM	FLOMON			
4000..7FFF	16K RAM				
8000..DFFF	24K	BANK			
E000..FFFF	8K	COMMON SPACE			

[illegible]